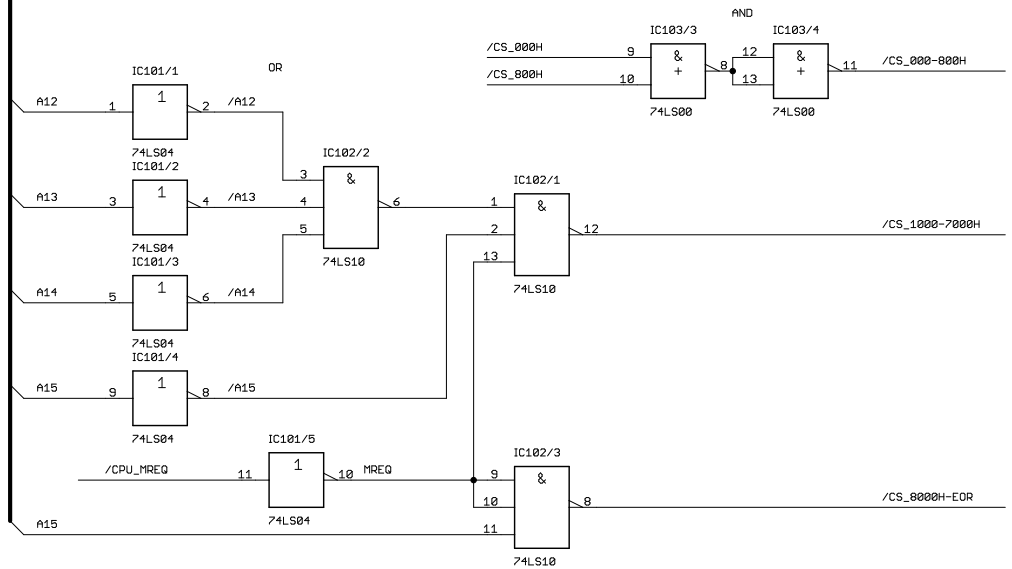
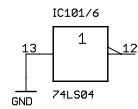
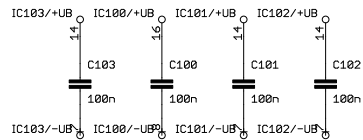
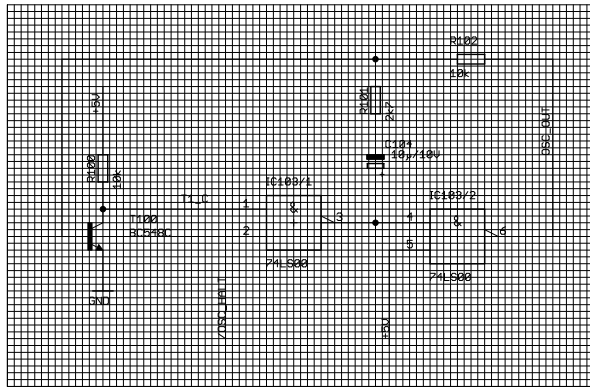


NOTE: hatched areas not fitted !



System TRAIN-Z: memory address decoder Copyright 2010 by Mario Blunk - electronics & IT engineering vist us at <a href="http://www.train-z.de">http://www.train-z.de</a>	
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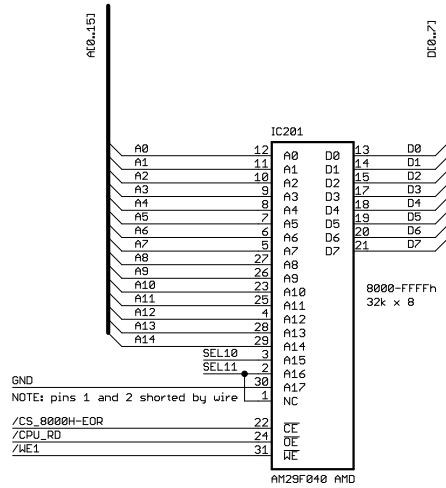
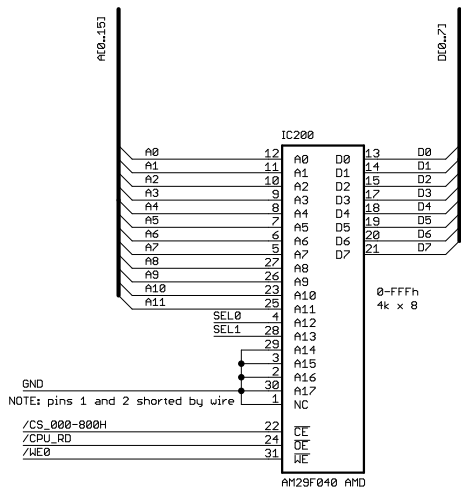
### BOOT PROM

### USER PROM

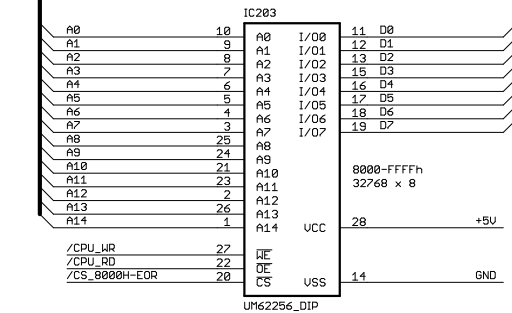
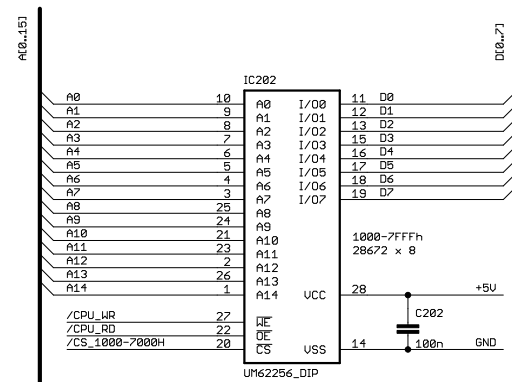
### static RAM

various device mounting possible  
address ranges must not overlap !  
see static RAM section !

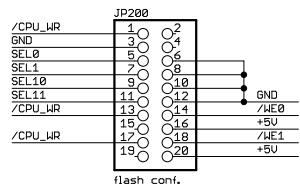
various device mounting possible  
address ranges must not overlap !  
see user PROM section !



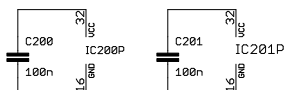
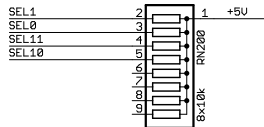
NOTE: IC201 may be AM29F040 (AMD) or W29C020 (WINBOND) or equivalent  
Please read manual for supported FLASH-device !



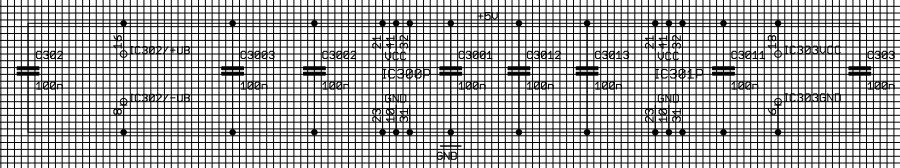
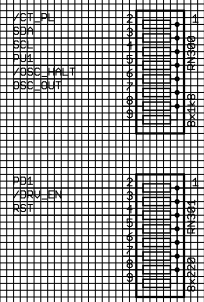
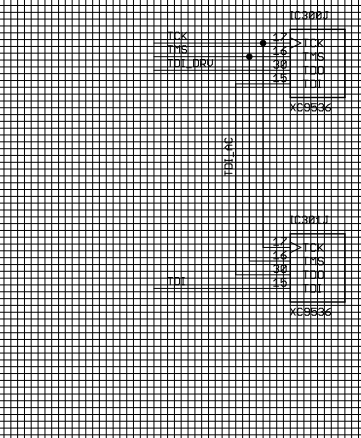
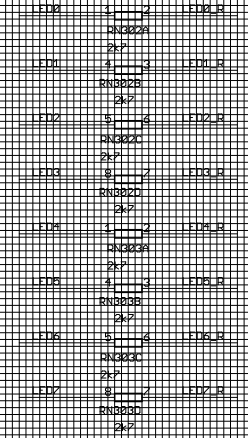
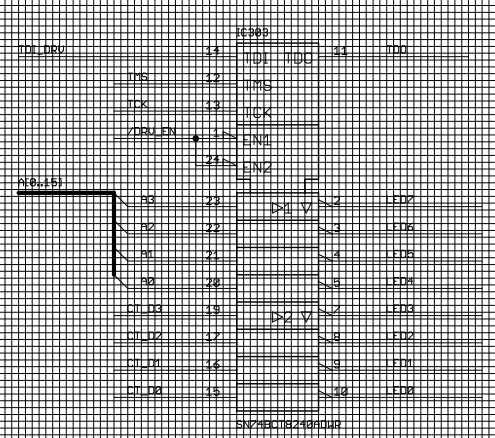
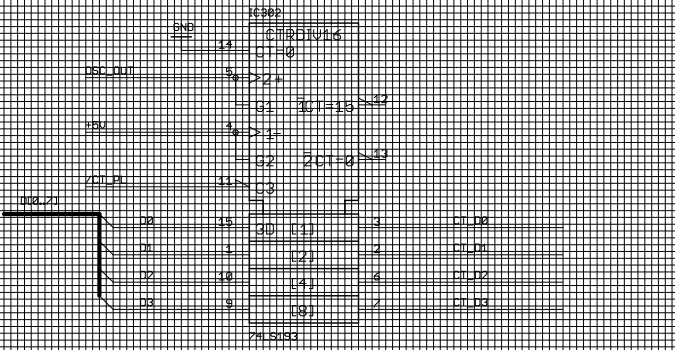
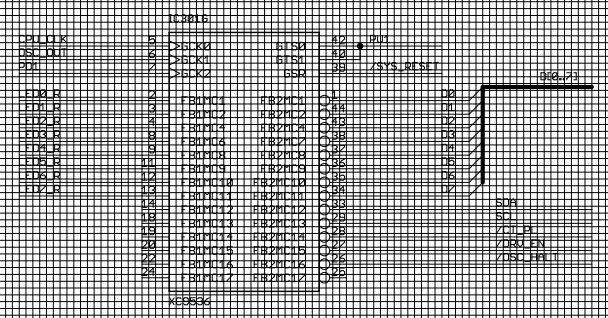
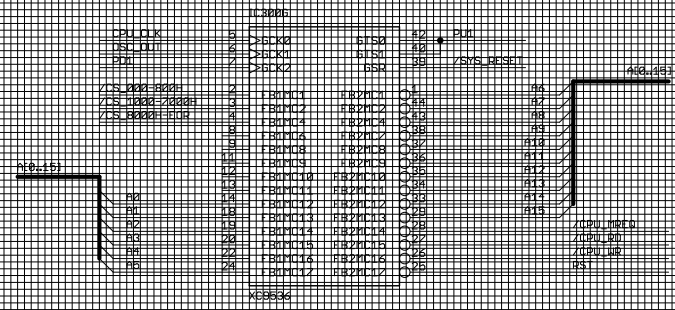
NOTE: IC200 may be AM29F040 (AMD) or W29C020 (WINBOND) or equivalent



flash conf.  
external write signal at 1-2  
close 5-6 / 7-8 for boot flash bank select  
close 9-10 / 11-12 for user flash bank select  
close 13-14 for boot flash write enabled  
close 14-16 for boot flash write protect  
close 17-18 for user flash write enabled  
close 18-20 for user flash write protect



FLASH-EEPROM, static RAM Copyright 2010 by Mario Blunk - electronics & IT engineering vist us at <a href="http://www.train-z.de">http://www.train-z.de</a>	
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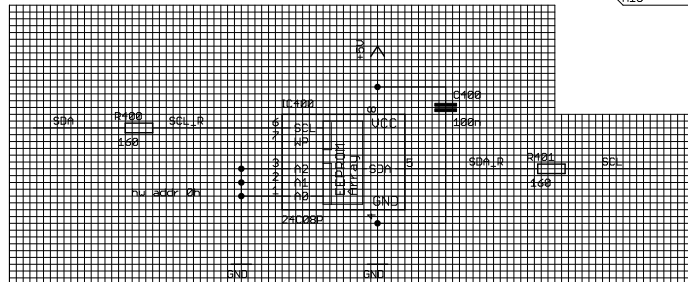
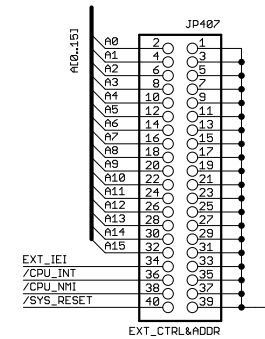
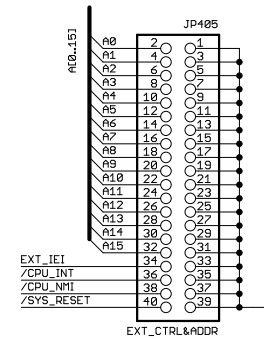
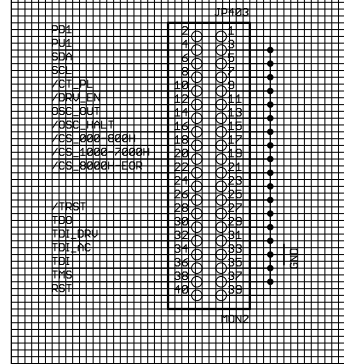
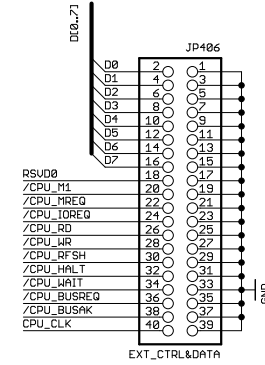
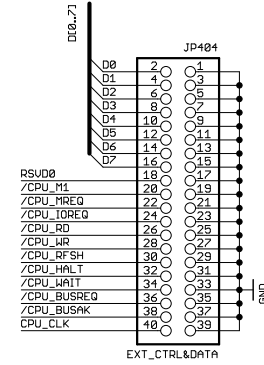
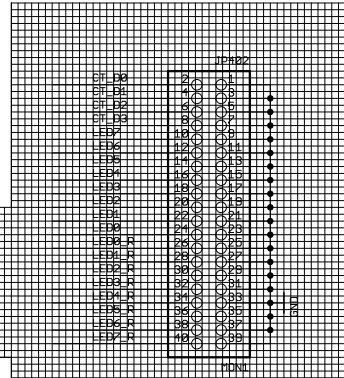
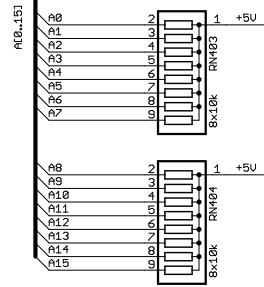
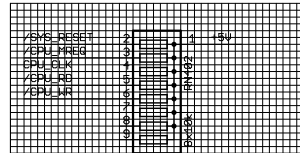
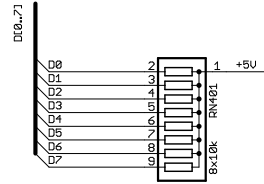
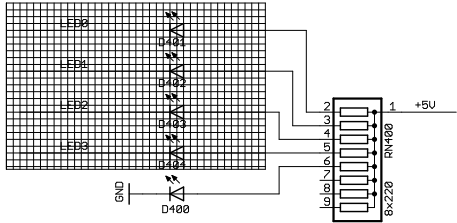


NOTE: hatched areas not fitted !

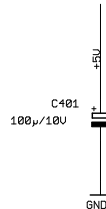
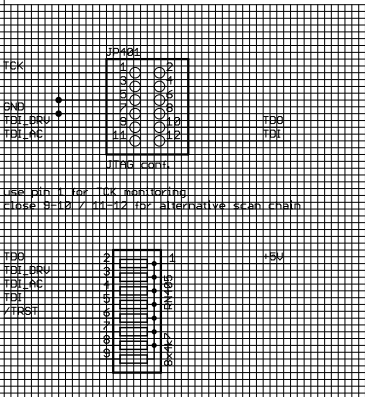
CPLDs, counter, driver, JTAG  
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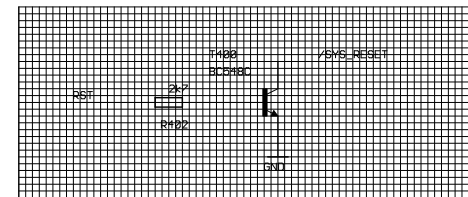
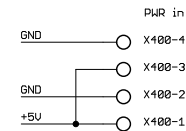
NOTE: RN400 replaced by a single 160 ohms resistor.



NOTE: hatched areas not fitted !



power supply



power, connectors, LEDs, JTAG  
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